

Notice of References Cited

Application/Control No.

O9/989,667

Examiner

Donna K. Mason

Applicant(s)/Patent Under
Reexamination
JOSEPH, FRANCIS

Art Unit
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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-2002/0129191	09-2002	DaCosta, Behram Mario	711/103
·	В	US-6,622,243	09-2003	Christeson, Orville H.	713/1
	C.	US-5,822,565	10-1998	DeRosa et al.	703/24
	D	ÙS-6,145,020	11-2000	Barnett, Philip C.	710/8
	Е	US-6,487,464	11-2002	Martinez et al.	700/79
	F	US-5,909,557	06-1999	Betker et al.	710/104
	G	US-2003/0033514	02-2003	Appleby-Allis et al.	713/100
	н	US-2002/0138720	09-2002	Yu et al.	713/100
	1	US-			
	J	US-			
	К	US-			
	L	US-			
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	, Country	Name	Classification
	Ν					
	0					
	Р					
	Q	·				
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)					
	U	Asari, K., et al., "Multi-Mode and Multi-Level Technologies for FeRAM Embdedded Reconfigurable Hardware," Feb. 15-17, 1999, IEEE International Solid-State Circuits Conference, Digest of Technical Papers, p. 106-107.					
	٧	Asari, K., et al., "FeRAM Circuit Technology for System on a Chip," July 21, 1999, Proceedings of the First NASA/DoD Workshop on Evolvable Hardware, p. 193-197.					
	w	"Ultrafast Nonvolatile Ferroelectric Information Storage Device," Nov. 1994, IBM Technical Disclosure Bulletin, Vol. 37, Issue 1 p. 421-424.					
	х						

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

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